



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Koji YAMAGUCHI

Application No.: 10/753,357

Filed: January 9, 2004

Docket No.: 118292

For: WIRING SUBSTRATE, SEMICONDUCTOR DEVICE, SEMICONDUCTOR
MODULE, ELECTRONIC EQUIPMENT, METHOD FOR DESIGNING WIRING
SUBSTRATE, METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE,
AND METHOD FOR MANUFACTURING SEMICONDUCTOR MODULE

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to initial examination, please consider the following:

Amendments to the Specification;

Amendments to the Claims as reflected in the listing of claims;

Amendments to the Drawings include an attached replacement sheet; and

Remarks.